

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10591724	
	Filing Date		2006-09-05	
	First Named Inventor	Kozo FUJIMOTO		
	Art Unit			
	Examiner Name			
Attorney Docket Number		FUJI-396		

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	1	Relevant portion of International Search Report of corresponding PCT Application PCT/JP2004/002538	<input type="checkbox"/>
	2	Kozo FUJIMOTO et al., "Research on Electronic Device Implementation Process Using Low-Melting Metallic Thin Layer and Interface Properties thereof," Annual Report, November 1, 2002, pgs. 53-54, Issue No. 7, Cooperative Research Center for Advanced Science and Technology, Osaka University, Japan.	<input checked="" type="checkbox"/>
	3	Related co-pending U.S. Patent appl. SN 10/591,723; Kozo FUJIMOTO et al.; MANUFACTURING "METHOD OF SEMICONDUCTOR MODULE";	<input type="checkbox"/>

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Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

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- ☒ See attached certification statement.
- ☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☐ None

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A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Marc Rossi/	Date (YYYY-MM-DD)	2006-10-26
Name/Print	Marc Rossi	Registration Number	31923

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